EXHIBIT 7

Teledyne's Genie TS GigE Vision Camera infringement of the '790 patent	
Claim 1	**Evidence**
(1.0) An interface for receiving data from an image sensor having an imaging array and a clock generator for transfer to a processor system comprising:	The Genie TS GigE Vision Camera ("Genie Camera") provides an interface for receiving data from an image sensor having an imaging array and a clock generator for transfer to a processor system. For example, an image capturing subsystem of the Genie Camera has a CMOS image sensor that includes an imaging array and a clock generator. An image processing subsystem of the Genie Camera includes a processor that processes image data. The Genie Camera includes interface circuitry that receives image data from the image capturing subsystem and transfers the image data to the processor. The interface circuitry thereby enables the transfer of image data between the image capturing subsystem, which runs in a pixel clock domain, and the image processing subsystem, which runs in a processor clock domain.
(1.1) a memory for storing imaging array data and clocking signals at a rate determined by the clocking signals;	The Genie Camera provides a memory for storing imaging array data and clocking signals at a rate determined by the clocking signals. For example, the interface circuitry of the Genie Camera includes a buffer module that stores the image data that is received from the image capturing subsystem. The buffer module has control and clock signal inputs. The buffer module clocks its internal and external signals at a rate that is determined by the input clock signals. This enables the buffer module to store the image data at a rate that is in accordance with the pixel clock domain of the image capturing subsystem.
(1.2) a signal generator for generating a signal for transmission to the processor system in response to the quantity of data in the memory; and	The Genie Camera provides a signal generator for generating a signal for transmission to the processor system in response to the quantity of data in the

	memory. For example, the interface circuitry of the of the Genie Camera includes interface functionality that generates a signal when the buffer module has image data that is ready for transmission to the processor. The signal indicates that the buffer module has a frame or sub-frame of image data for the processor.
(1.3) a circuit for controlling the transfer of the data from the memory at a rate determined by the processor system.	The Genie Camera provides a circuit for controlling the transfer of the data from the memory at a rate determined by the processor system. For example, the interface circuitry of the of the Genie Camera includes timing and control functionality that controls the transfer of image data from the buffer module to the processor. The timing and control functionality enables the image data to be transferred at a rate determined by the processor system. This enables the processor to acquire the image data at a rate that is in accordance with the processor clock domain.